

台灣積體電路製造股份有限公司 江宏禮 博士

- Hung-Li Chiang received the B.S. degree in Electrical Engineering (EE) and the M.S. degree in Graduate Institute of Photonics and Optoelectronics (GIPO) from National Taiwan University (NTU), Taipei, Taiwan, in 2007 and 2009, respectively. He joined TSMC since 2010. From November 2010 to November 2017, he worked on pathfinding and preliminary assessments for the next-generation CMOS device, from 16nm to 5nm, in a cross-functional team, focusing on higher performance, lower power consumption, and cost reduction. Since December 2017, he moved on to the research in emerging memories and new computing devices with potential for being embedded in advanced technology nodes, considering target PPA (power-performance-area) to match the requirement for applications of AI and IoT. He received his Ph.D. degree in National Yang Ming Chiao Tung University (NYCU), with job in 2022. During his time in the research team, he has published/ co-authored more than 10 technical papers and received over 35 US patents.